

PACO 2019: 3rd Workshop on Power-Aware Computing

Tuesday, 5 November 2019

Day I: Session I - Main/groundfloor-V0.05/2+3 - Prigogine (09:50 - 11:50)

time	[id] title	presenter
09:50	[15] Exploiting Nested Task-Parallelism in the LU Factorization of Hierarchical Matrices	CARRATALÁ-SÁEZ, Rocío
10:20	[19] Unleashing the sptrsv method in FPGAs	FAVARO, Federico
10:40	[20] Towards an efficient many-core implementation of the IRKA	VALDÉS, Matías
11:00	[21] Automatic selection of GPU sparse triangular solvers based on energy consumption	MARICHAL, Raúl
11:20	[25] Ginkgo's load-balancing COO SpMV on NVIDIA and AMD GPU architectures	TSAI, YU-HSIANG COJEAN, Terry

Day I: Session II - Main/groundfloor-V0.05/2+3 - Prigogine (14:50 - 17:10)

time	[id] title	presenter
14:50	[22] Parallel multiprecision iterative Krylov subspace solver	VOLK, Xenia Rosa
15:20	[11] Energy-Time Analysis of Heterogeneous Clusters for EEG Classification	ORTEGA, Julio
15:50	[9] Convolutional Neural Nets for the Run Time and Energy Consumption Estimation of the Sparse Matrix-Vector Product	DOLZ, Manuel F.
16:20	[16] Revisiting the idea of multiprecision block-Jacobi preconditioning - where do we stand 2 years later?	ANZT, Hartwig
16:50	[26] In-application energy measurement on Megware SlideSX systems	KÖHLER, Martin